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In re PATENT APPLICATION of

Inventor(s) HAMADA

Appln. No. 09/663,340

Group Art Unit: 2826

Examiner: Mondt, Johannes P.

series code ↑ serial no.
Filed: September 15, 2000

Atty. Dkt. PM 0271420

M#

TITLE: POWER SEMICONDUCTOR DEVICE
AND PRODUCTION METHOD FOR THE
SAME

Date: March 12, 2004

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PAT-286 7/99

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Attorney's Docket 020847-0271420
Client Reference: TFN990143-US**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE****OFFICIAL**In re PATENT APPLICATION of:
HAMADA

Confirmation Number: 1868

Application No.: 09/663,340

Group Art Unit: 2826

Filed: September 15, 2000

Examiner: Mondt, Johannes P.

For: POWER SEMICONDUCTOR DEVICE AND PRODUCTION METHOD FOR
THE SAMECommissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450**AMENDMENT/RESPONSE TRANSMITTAL**

Transmitted herewith is an amendment/response for this application.

FEES

The fee for claims and extension of time (37 C.F.R. 1.16 and 1.17) has been calculated as shown below:


	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST NO. PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDIT. FEE
TOTAL	6	- 20	= 0	x \$ 18.00	= \$ 0.00
INDEP.	1	- 3	= 0	x \$ 86.00	= \$ 0.00
FIRST PRESENTATION OF MULTIPLE DEP. CLAIM +					\$ 290.00 = \$ 0.00
TOTAL ADDITIONAL CLAIM FEE					\$ 0.00
GRAND TOTAL					\$ 0.00

HAMADA -- 09/663,340
Client/Matter: 020847-0271420

FEE PAYMENT

Authorization is hereby made to charge the amount of \$0.00 to Deposit Account No. 033975. Charge any additional fees required by this paper or credit any overpayment in the manner authorized above. A duplicate of this paper is attached.

Date: March 12, 2004
PILLSBURY WINTHROP LLP
P.O. Box 10500
McLean, VA 22102
(703) 905-2045


JOHN P. DARLING
Reg. No. 44482

Attorney's Docket 020847-0271420
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Examiner: Mondt, Johannes P.

Title: POWER SEMICONDUCTOR DEVICE AND PRODUCTION METHOD FOR THE
SAME**AMENDMENT**Mail Stop Non-Fee Amendments
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In reply to the Office Action dated January 16, 2004, please amend the above-identified application as follows:

IN THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Previously presented) A semiconductor device comprising:
a body region of a first conductivity type formed in a semiconductor substrate and having a major surface opposite to a surface shared between the semiconductor substrate and the body region;
a plurality of trench gates extending through the body region;
a plurality of first semiconductor regions of a second conductivity type that is different from the first conductivity type, the first semiconductor regions having a first depth as measured from said major surface of the body region, at least a portion of the first semiconductor regions flanking the trench gates on both of their sides and being in contact with said trench gates via films bordering and insulating the trench gates;

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